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APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/604,587		07/31/2003	CHING-YU CHANG	10264-US-PA-1	1586
31561	7590	01/20/2004		EXAMINER	
•		TELLECTUAL PR	PERKINS, PAMELA E		
7 FLOOR-1, ROOSEVEI	,	SECTION 2	ART UNIT	PAPER NUMBER	
TAIPEI, 100 TAIWAN				2822	
				DATE MAILED: 01/20/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
Office Author O was a	10/604,587	CHANG, CHING-YU					
Office Action Summary	Examiner	Art Unit					
	Pamela E Perkins	2822					
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with the c	orrespondence address					
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a rep If NO period for reply is specified above, the maximum statutory period Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).  Status	136(a). In no event, however, may a reply be time.  In within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDONE!	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).					
1) Responsive to communication(s) filed on <u>01 A</u>	Responsive to communication(s) filed on <u>01 August 2003</u> .						
2a) ☐ This action is <b>FINAL</b> . 2b) ☐ This	action is non-final.						
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4) Claim(s) 1-9 is/are pending in the application.	Claim(s) <u>1-9</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdra	4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.	Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-9</u> is/are rejected.							
	•						
8) Claim(s) are subject to restriction and/	or election requirement.						
Application Papers							
9) The specification is objected to by the Examin							
	The drawing(s) filed on <u>01 August 2003</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.						
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority under 35 U.S.C. §§ 119 and 120							
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No.</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> <li>13) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application) since a specific reference was included in the first sentence of the specification or in an Application Data Sheet.</li> <li>37 CFR 1.78.</li> <li>a) The translation of the foreign language provisional application has been received.</li> <li>14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121 since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.</li> </ul>							
Attachment(s)							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Informal P	(PTO-413) Paper No(s) atent Application (PTO-152)					
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Application/Control Number: 10/604,587

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## **DETAILED ACTION**

This office action is in response to the filing of the application papers on 1 August 2003. Claims 1-9 are pending.

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chang (6,541,828) in view of Ko (6,479,864) and Li et al. (6,545,310).

Chang discloses a method of fabricating a non-volatile memory where a longitudinal strip stacked layer is formed over a substrate (40); forming a buried bit line (46) in the substrate on each side of the longitudinal strip; patterning the longitudinal strip to form a plurality of stacked blocks; forming a dielectric layer (48) over the substrate (40); forming a word line (44) over the dielectric layer (48). Chang further discloses forming the buried bit line using ion implantation with the longitudinal strip as a mask (Fig. 7B; col. 3, line 22 thru col. 4, line 41). Chang does not disclose the longitudinal strip having a cap layer, a conductive layer and gate dielectric layer and the word line connecting blocks on the same row serially.

Ko discloses a method of fabricating a non-volatile memory where a longitudinal strip stacked layer is formed over a substrate (12), wherein the longitudinal strip

includes a gate dielectric layer (42), a conductive layer (44) and a cap layer (48); and forming a dielectric layer (22) over the substrate (12). Ko further discloses the cap layer (48) having a greater etch rate than the dielectric layer (22). Ko also discloses conducting a wet or dry anisotropic etching process to remove the cap layers (Fig. 5; col. 7, line 36 thru col. 8, line 14).

Since Chang and Ko are both from the same field of endeavor, a method of fabricating a non-volatile memory, the purpose disclosed by Chang would have been recognized in the pertinent art of Ko. Therefore, it would have been obvious to one ordinary skill in the art at the time the invention was made to modify Chang by the longitudinal strip having a cap layer, a conductive layer and gate dielectric layer as taught by Ko to preventing layers not aligned with the selected pattern (col. 7, lines 65-67).

Li et al. disclose a method of fabricating a non-volatile memory where a longitudinal strip stacked layer is formed over a substrate and forming a word line (2) over the substrate to connect blocks on the same row serially (fig. 4; col. 5, lines 3-51).

Since Chang and Li et al. are both from the same field of endeavor, a method of fabricating a non-volatile memory, the purpose disclosed by Li et al. would have been recognized in the pertinent art of Chang. Therefore, it would have been obvious to one ordinary skill in the art at the time the invention was made to modify Chang by the word line connecting blocks on the same row serially as taught by Li et al. to obtain control the gated structures (col. 5, lines 5-30).

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## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Pamela E Perkins whose telephone number is (571) 272-1840. The examiner can normally be reached on Monday thru Friday, 9:00am to 5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on (571) 272-1852. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

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